

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,739,316 B2
APPLICATION NO. : 10/646940
DATED : June 15, 2010
INVENTOR(S) : J. Patrick Thompson et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Face page, in field (54), under "Title" column 1, line 2, after "OF" insert -- A --.

In column 1, line 2, after "OF" insert -- A --.

In column 116, line 41, below "logic circuits." insert -- APPENDIX A --.

In column 125, line 1, (APPENDIX B), after "using" insert -- in --.

Signed and Sealed this
Eighth Day of February, 2011



David J. Kappos
Director of the United States Patent and Trademark Office